

GR 97 P 1049 D



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernhard Schätzler et al.
Applic. No. : 09/688,465
Filed : October 16, 2000
Title : Electronic Component With An Integrated Circuit Mounted On
An Island Of A Lead Frame
Examiner : Nitin Parekh Group Art Unit : 2811

DECLARATION UNDER 37 C.F.R. § 1.131

The undersigned, Bernhard Schätzler, Georg Ernst, inventors of this invention, hereby declare that:

The invention of the above-identified application was conceived and "reduced to practice" at least as early as January 4, 1996.

The undersigned, Bernhard Schätzler, Georg Ernst, developed an electronic component with an integrated circuit mounted on an island of a lead frame according to the invention and filed a patent application on July 17, 1995 in German Patent Office. This application was published on January 23, 1997, which is one day later than the filing date January 22, 1997 of the PCT application PCT/DE97/00105 which provides the priority date for the instant application.

Enclosed, as corroborating evidence, is the published German application DE 195 26 010 A1 as well as an English translation thereof.

The undersigned hereby declare that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Bernhard Schätzler

Georg Ernst

Date

Date

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